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**In the United States Patent and Trademark Office**

**Serial number** 09/340,172

**Application filed** June 25, 1999

**Applicant** Derek Wong

**Application Title** Methods for Increasing Instruction-Level Parallelism in Microprocessors and Digital Systems

**Examiner/GAU** Eric Coleman / 2183

**RECEIVED**

DEC 31 2003

**Mailed** December 22, 2003

Technology Center 2100

**Petition for Extension of Time (Rules 136 and 17(a)-(d))**

**Outstanding Office Action mailed** August 21, 2003

**Original period for response expired** October 21, 2003

**Request for extension of two months to** December 22, 2003

**Small Entity Fee enclosed** \$210

**To:**

Assistant Commissioner for Patents  
United States Patent and Trademark Office  
Washington, DC 20231

**Sir:**

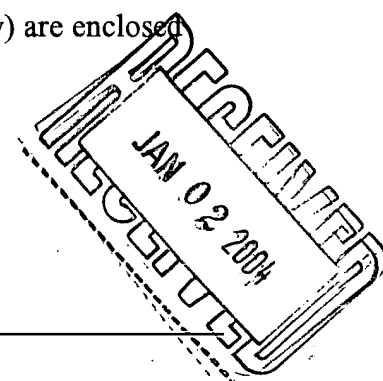
In the above application, applicant respectfully petitions that the period for response to the outstanding Office Action indicated above be extended for the additional months indicated above. December 21, 2003 was a Sunday, so this mailing is on December 22, 2003.

A response to such Office Action and the above Petition fee (Small Entity) are enclosed herewith.

Very Respectfully,

*Derek Wong*

Applicant Derek Wong  
Telephone 408-927-7940



**Express Mail Label #** ET727223990US **Date of Deposit** December 22, 2003

I hereby certify that this paper or fee is being deposited with the United States Postal Service using "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to "Assistant Commissioner for Patents, Washington, DC 20231."

Signed

*Derek Wong*

Inventor

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